# **CPCI-CPU/5201** CompactPCI<sup>®</sup> PowerPC<sup>™</sup> Board with CAN, ETHERNET and USB



# Cost effective 3U/4HP CompactPCI Board

- MPC5121e CPU with e300 core and 400 MHz / 760 MIPS
- Flexible and fast storage via CompactFlash®-card and USB connector
- CAN, 1 Mbit/s, electrically isolated
- Linux<sup>®</sup>,QNX<sup>®</sup> and VxWorks<sup>®</sup> BSPs available
- CANopen<sup>®</sup> and DeviceNet<sup>™</sup> available, on-board web server available

# Longevity Program of Freescale™

- · Enhanced availability: the CPU is included in the longevity program of Freescale
- Low power consumption

## Wide Storage Support (ATA, SDHC<sup>™</sup>)

- On request ATA devices and SDHC CLASS 10
- · CompactFlash Card slot
- On-board storage options eMMC<sup>™</sup> and Magnetoresistive RAM (MRAM)

#### CompactPCI PowerPC Board

This board is specially designed for cost sensitive applications with low power consumption and a long product availability.



The Freescale RISC microcontroller MPC5121e with FPU and fast flash memory support is best suited for data processing purposes.

#### **Network Interfaces**

The CPCI-CPU/5201 contains one ETHERNET interface for 10/100 Mbit/s nets that is accessible via a RJ45 connector in the front panel. The ISO11898 compatible CAN interface is accessible via a DSUB9 connector in the front panel. The CAN interface is electrically isolated and supports bit rates up to 1 Mbit/s.

Additionally a second CAN and a serial interface (CAN1/SER1) could be connected via a 3U / 4HP add-on card.

## MPC5121 Integrated Processor

The MPC5121 CPU contains the e300 Power Architecture® technology processor core and operates with 400 MHz and up to 760 MIPS. It is equipped with 32-Kbyte instruction cache and 32-Kbyte data cache. The superscalar processor core comes with instruction and data MMU and integrated double-precision floating-point unit.

#### Software Support

The flash memory carries the standard 'U-Boot' program that enables the CPCI-CPU/5201 to boot various operating systems from network, on-board Flash or SD<sup>™</sup> card. Thus Linux and Real-time OS like QNX and VxWorks are directly supported with full support of on-board drivers by esd, others on request. There is also a bunch of higher layer protocols like CANopen, DeviceNet as well as an onboard web server available.

(This product is under development. It will be available Q2 2014.)

Order No.

1.2404.02

1.2404.01

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1.2301.03



Conservation

## **Technical Specifications:**

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Compactron		General.		
Microcontroller	r Freescale MPC5121e, 400 MHz, e300 core, cache: 32 KB / 32 KB, FPU	Ambient temperature	0 +55 °C, on request: -40 °C convection cooled	. +75 °C
Memory	SDRAM: 512 Mbyte DDR2, 200 MHz, NOR Flash: 4 Mbyte; NAND Flash: 128 Mbyte; CF-card connector, eMMC (I.2024.01 only): up to 64 Gbyte On request: MRAM: 512 kbyte; NAND-Flash with ATA controller, 32 Gbyte; SDHC slot: more than 10 MB/s (r/w) capable	Relative humidity	Max. 90 % (non-condensing)	
		Power supply	3.3V, tbd. W <sub>TYP</sub> , tbd. W <sub>STANDBY</sub> , 5 V, tbd. W <sub>TYP</sub> , tbd. W <sub>STANDBY</sub> ,	
		Dimensions	3U / 4TE CompactPCI	
		Order Information:		
RTC	Battery buffered real-time clock	Hardware		Order
PCI	PCI 2.3, 32 bit, 33 / 66 MHz, 3.3V signaling environment (not 5V tolerant), 7x external bus master support			
		CPCI-CP0/5201	MPC5121, 400 MHZ	1.2404
		CPCI-CPU/5201-eMM	IC MPC5121, 400 MHz, eMMC	1.2404
Interfaces:		Software Support		
ETHERNET	1x 10BASE-T/100BASE-TX, IEEE 802.3, RJ45 connector with LEDs		Linux DOD/s de stis s	10404
		CPCI-CPU/5201-LINUX	Linux BSP/adaption	1.2404
USB	1x USB 2.0 controller, high-speed (480 Mbit/s), USB-A connector type	CPCI-CPU/5201-QNX	QNX BSP/adaption	I.2404.
		CPCI-CPU/5201-VxW	VxWorks BSP/adaption	I.2404
Service	1x RS-232 via RJ12 connector	Accessories		
CAN	1x CAN, 1 Mbit/s, electrically isolated, ISO11898, 9-pin DSUB	CPCI-CAN-ISO-11898	CAN and RS-232 adapter,	I.2301.

3 U / 4 HP front panel CANopen® is a registered community trademark of CAN in Automation e.V.. All trademarks are reserved by their respective owners.

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